Electronic Pate	nt Appl	ication Fe	e i ransmit	tai				
Application Number:	1059	10591152						
Filing Date:	01-Ju	01-Jun-2007						
Title of Invention:	Clock	Clock Recovery Circuit And Receiver Using The Circuit						
First Named Inventor/Applicant Name:	Hide	Hideki Nakahara						
Filer:	Josep	Joseph W. Price/Sharon Farnus						
Attorney Docket Number:	5047	50478-3900						
Filed as Large Entity								
U.S. National Stage under 35 USC 371 Fili	ing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:					<u> </u>			
Claims:								
Independent claims in excess of 3		1614	1	220	220			
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Post-Allowance-and-Post-Issuance:								
Extension of Time:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			220